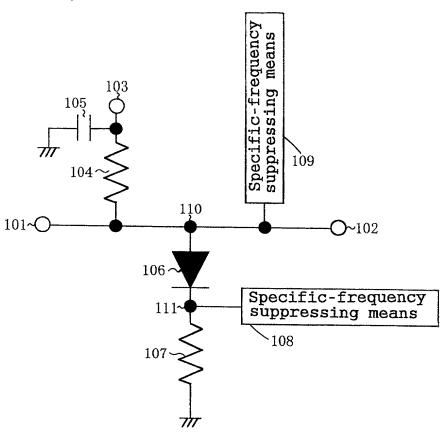
60111811

Fig. 1 (a)



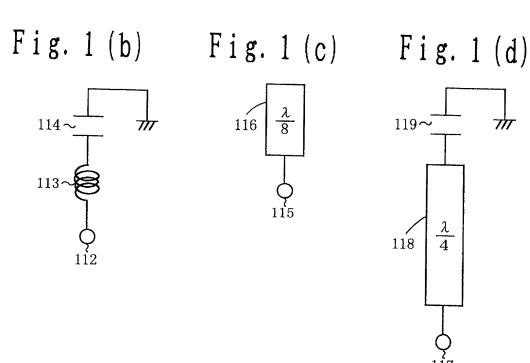


Fig. 2 (a)

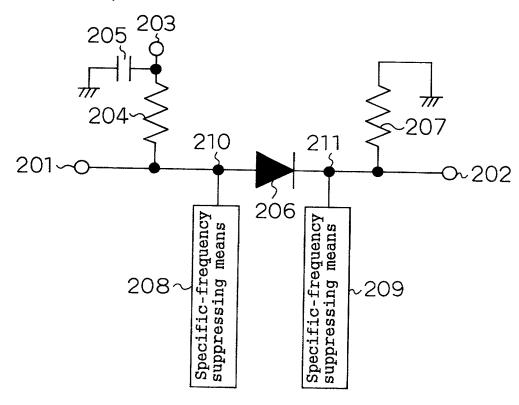
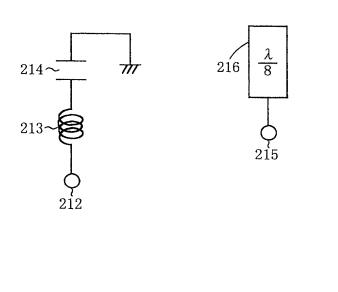


Fig. 2 (b)

Fig. 2 (c) Fig. 2 (d)



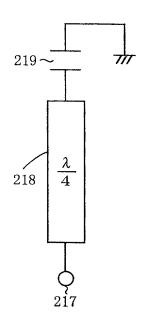


Fig. 3 (a) Specific-frequency suppressing means Specific-frequency suppressing means \sim 311

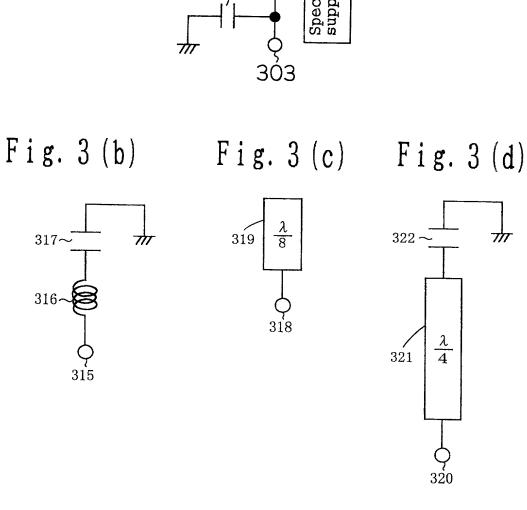
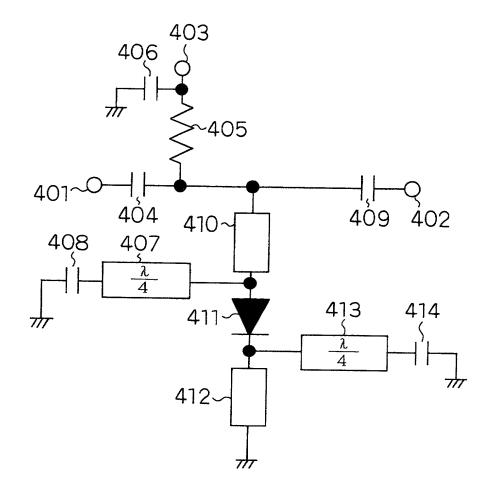


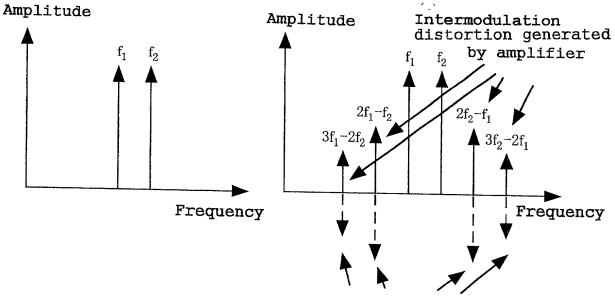
Fig. 4



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Fig. 5 (b)

Fig. 5 (c)



Intermodulation distortion generated by predistortion circuit

116.0

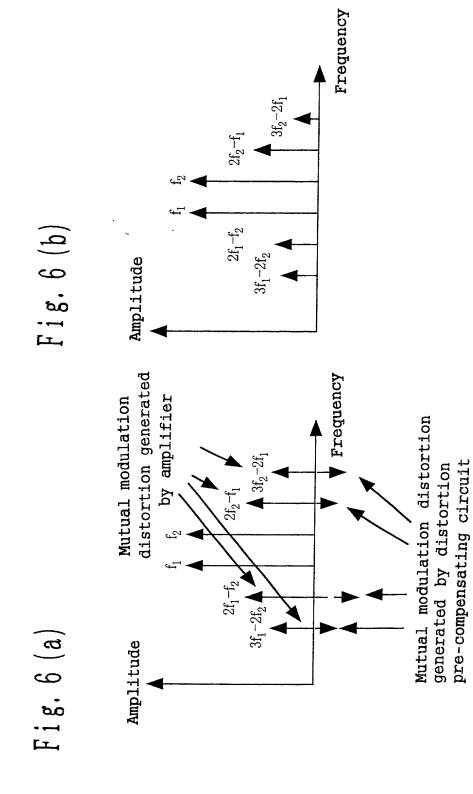


Fig. 7

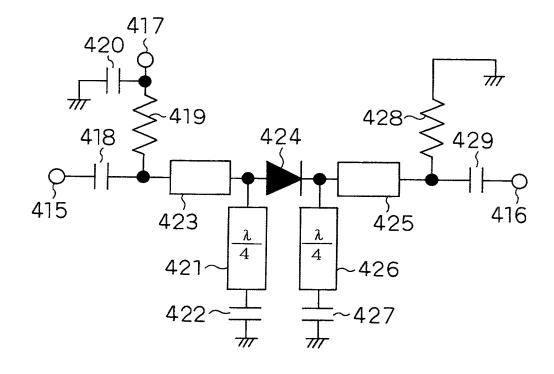


Fig. 8

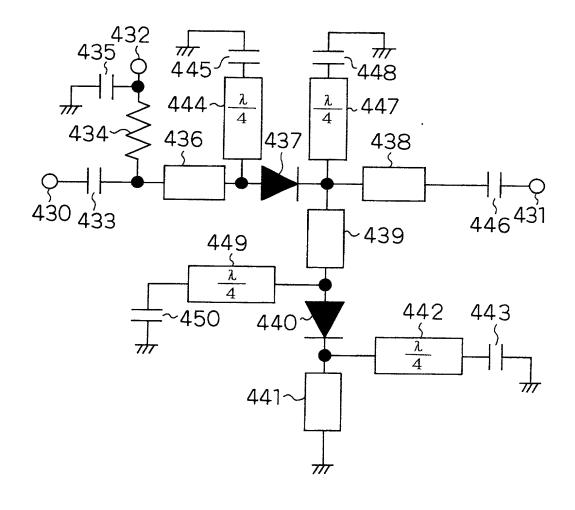
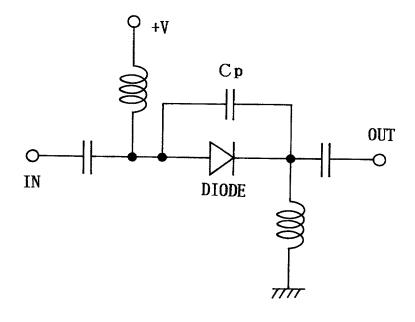
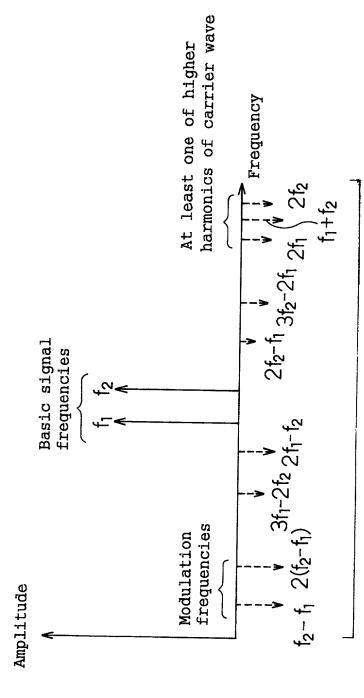


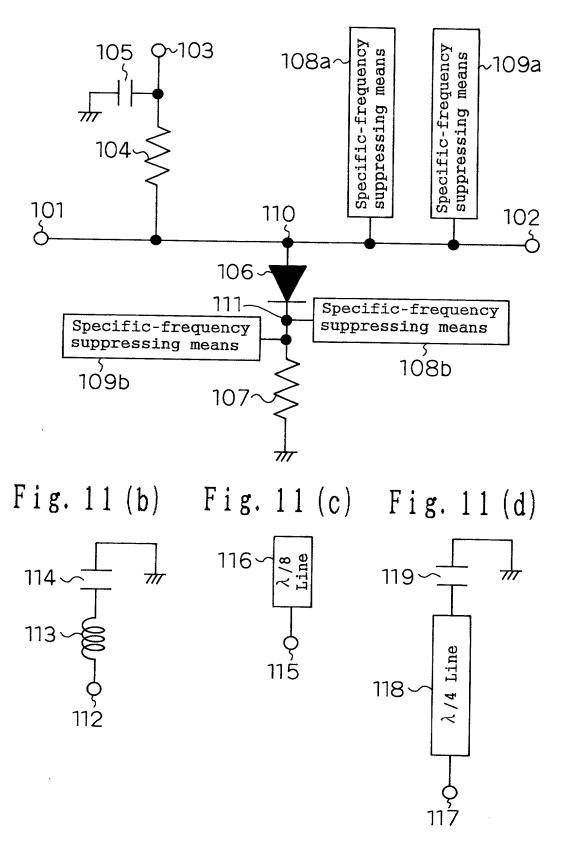
Fig. 9





Signals generated by predistortion circuit

Fig. 11 (a)



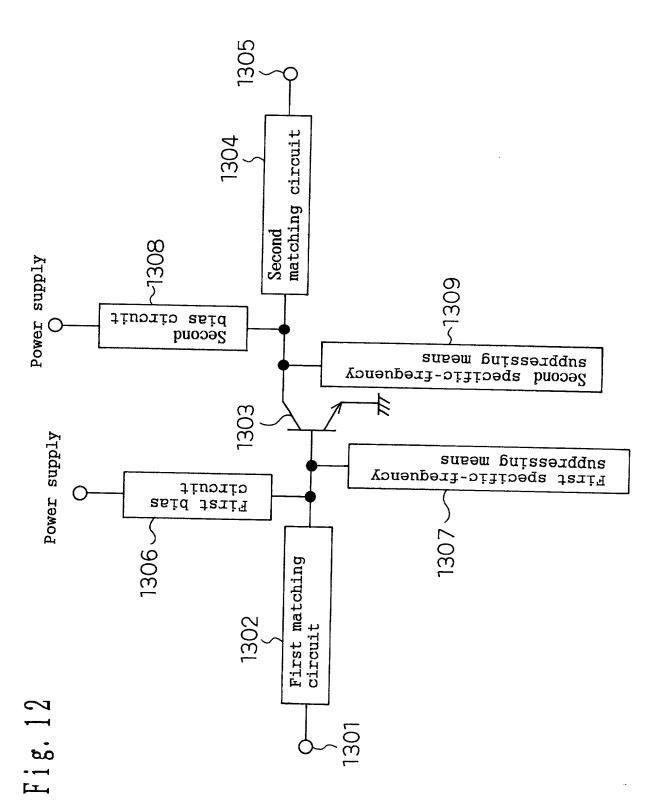


Fig. 13

